

INTEL/16406

10/601,439

Applicant

Ju

Filing Date

June 23, 2003

Group Art Unit

~~2183~~ 2192

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

*Examiner Initials	Document Number	Publication Date	Country	Class	Subclass	Translation Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

MY	C01	Kemal Ebcioglu and Erik R. Altman, <i>Daisy: Dynamic Compilation for 100% Architectural Compatibility</i> , IBM Research Division (August 5, 1996).				
MY	C02	Bich C. Le, <i>An Out-of-Order Execution Technique for Runtime Binary Translators</i> , Hewlett Packard Company (1998).				
MY	C03	Erik M. Nystrom et al., <i>Code Reordering and Speculation Support for Dynamic Optimization Systems</i> , University of Illinois, Urbana-Champaign.				

EXAMINER

Michael Yzardall

DATE CONSIDERED

9/16/06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.